

LAYERED PRESSURE SENSITIVE TRANSDUCER AND METHOD FOR MAKING SAME

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Inventor(s): YANIGER STUART I
Applicant(s): INTERLINK ELECTRONICS INC (US)
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Abstract

A variable resistance transducer responsive to an applied pressure is disclosed comprising a smooth resistive layer on a flexible substrate and deposited thereon by printing or imaging technology, small points of nonconductive or semiconductive material.

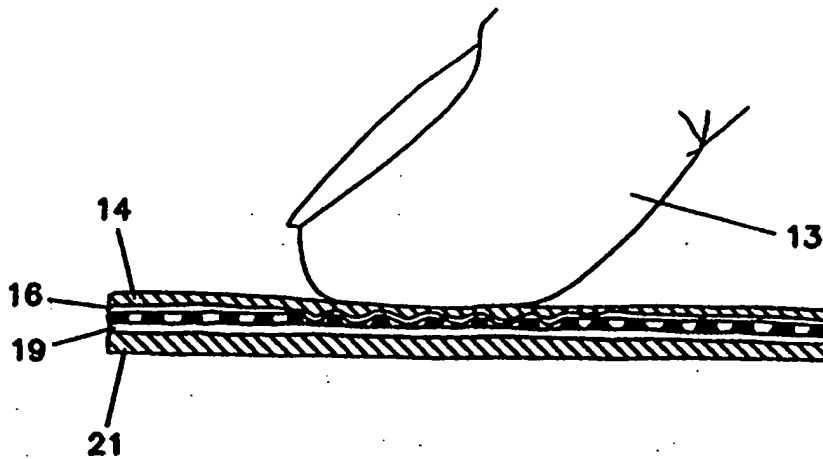
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<p>(21) International Application Number: PCT/US95/01972 (22) International Filing Date: 16 February 1995 (16.02.95) (30) Priority Data: 08/198,149 17 February 1994 (17.02.94) US (71) Applicant: INTERLINK ELECTRONICS, INC. [US/US]; 546 Flynn Road, Camarillo, CA 93012 (US). (72) Inventor: YANIGER, Stuart, I.; 2402 Katherine Avenue, Ventura, CA 93003 (US). (74) Agent: BERMAN, Charles; Merchant, Gould, Smith, Edell, Welter & Schmidt, Suite 400, 11150 Santa Monica Boulevard, Los Angeles, CA 90025-3395 (US).</p>		<p>(81) Designated States: CA, JP, European patent (AT, BE, CH, DE, DK, ES, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE).</p> <p>Published <i>With international search report. Before the expiration of the time limit for amending the claims and to be republished in the event of the receipt of amendments.</i></p>

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A variable resistance transducer responsive to an applied pressure is disclosed comprising a smooth resistive layer on a flexible substrate and deposited thereon by printing or imaging technology, small points of nonconductive or semiconductive material.